

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) An image processing circuit, comprising:
 - a control-signal generating device that generates a control signal indicating a type of an electro-optical panel used in combination with the image processing circuit;
 - a D/A conversion device that converts input image data from a digital signal to an analog signal to generate an image signal and that adjusts a range where a signal level of the image signal is changed, according to the control signal; and
 - a processing device that generates an output image signal to be sent to the electro-optical panel, according to the image signal.
2. (Original) An image processing circuit according to Claim 1, the processing device further comprising:
 - an image-signal inversion section that inverts a signal polarity of the image signal at an inversion period determined in advance, with a certain potential being used as a reference while amplifying the image signal to generate an inverted image signal;
 - a reference-signal generating section that generates a first reference voltage and a second reference voltage according to the control signal, and that alternately selects one of the first reference voltage and the second reference voltage at the inversion period to generate a reference signal; and
 - an output-image-signal generating section that synthesizes the inverted image signal with the reference signal to generate the output image signal.
3. (Original) An image processing circuit according to Claim 2, the reference-signal generating section further comprising:

a power-supply section that generates a positive-polarity reference voltage higher than a reference potential determined in advance according to the type of the electro-optical panel by a minimum applied voltage, and that generates a negative-polarity reference voltage lower than the reference potential by the minimum applied voltage;

a first selection section that selects a voltage corresponding to the electro-optical panel used in combination with the image processing circuit among the positive-polarity reference voltages, according to the control signal to generate the first reference voltage, and that selects a voltage corresponding to the electro-optical panel used in combination with the image processing circuit among the negative-polarity reference voltages, according to the control signal to generate the second reference voltage; and

a second selection section that alternately selects one of the first reference voltage and the second reference voltage at the inversion period to generate the reference signal; and

wherein the minimum applied voltage is specified for each electro-optical panel, and is the lowest voltage required to be applied to the electro-optical material of the electro-optical panel to obtain a range of transmittance used for displaying images.

4. (Original) An image processing circuit according to Claim 3, the minimum applied voltage being a voltage corresponding to a saturation transmittance of the electro-optical material.

5. (Original) An image processing circuit according to Claim 3, the power-supply section comprising:

a first voltage source that generates a first voltage higher than a reference potential determined in advance according to the type of the electro-optical panel by a maximum applied voltage;

a second voltage source that generates a second voltage lower than the reference potential by the maximum applied voltage;

a subtraction section that subtracts a change voltage determined in advance according to the type of the electro-optical panel from the first voltage to generate the positive-polarity reference voltage; and

an adder section that adds the change voltage to the second voltage to generate the negative-polarity reference voltage; and

wherein the maximum applied voltage is the highest voltage required to be applied to the electro-optical material to obtain a range of transmittance used to display images, according to the type of the electro-optical panel.

6-15. (Canceled)

16. (Original) An image processing method that generates an output image signal to be sent to one type of electro-optical panel selected from among a plurality of types of electro-optical panels determined in advance and having electro-optical materials in which their transmittances are changed according to an applied voltage, the image processing method comprising the steps of:

converting image input data from a digital signal to an analog signal to generate an image signal, and adjusting a range where the signal level of the image signal is changed, according to the type of the electro-optical panel;

inverting the signal polarity of the image signal with a certain potential being used as a reference at an inversion period determined in advance while amplifying the image signal to generate an inverted image signal;

alternately selecting one of a positive-polarity reference voltage higher than a reference potential determined in advance according to the type of the electro-optical panel by a minimum applied voltage, and a negative-polarity reference voltage lower than the

reference potential by the minimum applied voltage, at the inversion period to generate a reference signal; and

synthesizing the inverted image signal and the reference signal to generate the output image signal;

wherein the minimum applied voltage is specified for each electro-optical panel, and is the lowest voltage required to be applied to the electro-optical material to obtain a range of the transmittance to be used to display images.

17-18. (Canceled)

19. (Original) An electro-optical device, comprising:

an image processing circuit according to Claim 1; and

an electro-optical panel having an electro-optical material in which a transmittance of the electro-optical material is changed according to an applied voltage, and receiving the output image signal.

20. (Original) An electro-optical device according to Claim 19, the electro-optical panel further comprising:

a device substrate including a plurality of data lines, a plurality of scanning lines, switching devices disposed at the intersections of the plurality of data lines and the plurality of scanning lines, and pixel electrodes connected to the switching devices;

an opposing substrate having an opposing electrode; and

an electro-optical material sandwiched by the device substrate and the opposing substrate,

the reference potential being the potential of the opposing electrode, and

the output image signal being sequentially sent to the plurality of data lines.

21. (Original) An electronic apparatus comprising an electro-optical device according to Claim 19.

22. (Original) A projection-type display apparatus, comprising:
a light source;
an electro-optical device according to Claim 19 that modulates light emitted from the light source; and
a projection-lens system that projects light emitted from the electro-optical device.

23. (Previously Presented) An image processing circuit according to Claim 1, further comprising:
a phase-development circuit that applies phase development to the image signal whose range is adjusted by the D/A conversion device, to generate phase-development image signals.

24. (Previously Presented) An image processing circuit according to claim 1, the processing device generating the output image signal according to a voltage-transmittance characteristic of the electro-optical panel.

25-26. (Canceled)

27. (New) An image processing circuit according to claim 1, the image processing circuit being capable of being used with a plurality of different types of panels, the electro-optical panel used in combination with the image processing circuit being one of the plurality of different types of panels.